# Bit assignment register: *QE\_config* Date: 08/2/21 Version: 0.2

**QE enable**

0 = Disable

1 = enable

|  |  |  |
| --- | --- | --- |
|  | Bit |  |
|  | 0 |  |
|  | 1 |  |
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|  | 31 |  |

**QE signal source**

0 = External

1 = Internal simulated

**QE simulate enable**

0 = disable internal simulator

1 = enable internal simulator

**QE simulate direction**

0 = Clockwise

1 = Anti-clockwise

**QE simulate swap A/B**

0 = No swap

1 = Swap

**Filter sample size**

000 = 2 value average

001 = 4 value average

010 = 8 value average

011 = 16 value average

100 = 32 value average

other = 32 value average

**QE speed calc enable**

0 = Disable speed calculation

1 = Enable speed calculation

**QE enable filter**

0 = Disable filter

1 = Enable filter